# STRUCTURE OF

## B.E. (ELECTRONICS) 2008 COURSE

### TERM - I

<table>
<thead>
<tr>
<th>SUBJECT CODE</th>
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<th>TH</th>
<th>PR</th>
<th>TUT</th>
<th>PP</th>
<th>TW</th>
<th>OR</th>
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<td>404201</td>
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|               | TOTAL                      | 19 | 8  | 1   | 500| 100| 50 | 100| 750         |

**Elective _I**

1. Advanced Measurement Systems
2. Advanced Power electronics
3. Biomedical Instrumentation
4. Mechatronics

**Elective-II**

1. Advanced Computer Architecture
2. Entrepreneurship and Business Planning
3. SOC (System on Chip)
4. Robotics and Automation
SEM-I

ELECTRONIC SYSTEM DESIGN (404201)

Teaching Scheme

Lectures/Week: 3Hrs
Tutorial/Week: 1Hrs.

Examination Scheme

Paper: 100 Marks
Oral: 25 Marks

Unit 1: Introduction


Unit 2: Hardware Designs- Analog

Analog Signal Conditioning: Factors affecting choice of Op-Amps in signal conditioning applications. Need for Instrumentation Amplifiers- Case study. Error budget analysis with Case study. ADCs: Interpretation of ADC specifications from design view point. Considerations in selecting References (Vref for ADC). DACs: Interpretation of DAC specifications from design view point.

Unit 3: Hardware Design- Digital

Interface examples for LED, HB LED, LCD, Keyboard, Relays (Electromagnetic and Solid State). Microcontrollers: Comparative study of different Microcontroller Architectures. Factors affecting choice of Microcontroller for particular application with Case study of one application. Introduction to buses and protocols used in Electronic Products- I2C, SPI.

Unit 4: Software Design and Testing for Electronic Product

Different approaches to development of application software for Electronic Product. Assemblers Factors affecting choice between Assembly language and High level language like C and C++. Documentation practices and templates for above software. Debugging tools and techniques for software- Features of Simulators, ICE, IDE.
Unit 5: PCB Design and EMI/EMC

PCB Design practices for Analog and Mixed signal circuits: Ground Loops, Precision circuits, shielding and guarding. PCB Design Practices for High speed digital circuits Signal integrity and EMC, EMI/EMC testing standards and compliance

Unit 6: Fault Finding and Testing

2. Paul Horowitz, _Art of Electronics_, Cambridge University Press

Reference Books


Tutorials

1. Power supply sizing (Estimation of current requirement)
2. Design of SPAN ZERO circuit
3. Design of Transducer interface using Whetstone Bridge
4. Error budget analysis
5. ADC Interface example
6. DAC interface example
7. Interfaces - LED, HB LED, LCD, Relays
8. Case study for deciding appropriate Microcontroller for given application
9. PCB Design for Mixed Signal Circuit (Involving ADC and Signal Conditioning)
10. DC analysis of given circuit
11. AC analysis of given circuit
12. Sensitivity analysis for given circuit
13. Reliability calculations from given data
Unit 1: Digital CMOS Design

CMOS Inverter, voltage transfer curve, body effect, hot electron effect, velocity saturation, Static and dynamic dissipations, Power delay product. Noise margin, Combinational logic design, W/L calculations, Transmission gate, design using TGs, parameter, layout, Design Rule Check, Technology scaling.

Unit 2: CMOS Memories

Random Access Memories (RAM), Static RAM (SRAM), standard architecture, 6T cell, sense amplifier, address decoders, timings. Dynamic RAM (DRAM), different DRAM cells, refresh circuits, timings. Role of memories in PLD, chip area occupied by them.

Unit 3: VHDL and Finite State Machines

VHDL design units, modeling styles, synthesizable and non synthesizable test benches, design flow, functions, procedures, attributes, test benches, configurations, packages, Synchronous and asynchronous machines, Finite State Machines (FSM), metastability, state diagrams and VHDL codes for FSMs.

Unit 4: Programmable Logic Devices (PLDs)

Need of PLD, antifuse, SRAM and flash technologies, Comparison with ASIC, general purpose processor, DSP processor, microcontroller, memories etc, Features, specifications, detail architectures, application areas, limitations of Complex Programmable Logic Device (CPLD) and Field Programmable Logic Devices (FPGA).

Unit 5: Fault tolerance and testability

Types of fault, stuck open, short, stuck at 1, 0 faults, Fault coverage, Need of Design for Testability (DFT), Controllability, predictability, testability, built in Self Test (BIST), Partial and full scan check, Need of boundary scan check, JTAG, Test Access Port (TAP) controller.
Unit 6: Signal Integrity and System on Chip

Clock skew, Clock distribution techniques, clock jitter, Supply and ground bounce, power distribution techniques, Power optimization, Interconnect routing techniques, wire parasitics. Design validation, Off chip connections, I/O architectures, Signal integrity issues, EMI immune chip design.

Text books


Reference Books

1. Allen Holberg, _Analog CMOS Design_, Oxford University Press.
4. Data Sheets of PLDs.

List of Practicals

**Group A**: To write VHDL code and test bench, synthesis, simulate and download in to PLD, for the following (Any four).
1. To sense physical parameter such as temperature/pressure/flow etc., convert in to digital using ADC, interface to PLD and display.
2. To write/read in to RAM.
3. To generate ramp/square waveform using DAC.
4. To measure the period of a signal.
5. To design lift/traffic light controller.
6. To design programmable timer/counter.

**Group B**: To design following logic, calculate W/L ratios, prepare layout in multi metal layers and simulate (Any four).
Assume suitable technology, load capacitance, free running frequency, switching timings etc.
1. CMOS Inverter.
2. CMOS NAND, NOR.
3. 2:1 Mux by conventional method and by using Transmission gates. Comparison of them.
4. CMOS Combinational logic for minimum 4 variables.
5. Minimum 5 stage cascaded Inverter ring counter and understand technology limitations.
6. Clock skew generation and mitigation by any one method for synchronous machine.
## EMBEDDED SYSTEMS (404203)

<table>
<thead>
<tr>
<th>Teaching Scheme</th>
<th>Examination Scheme</th>
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<tr>
<td>Lectures: 4 Hrs/week</td>
<td>Paper: 100 Marks</td>
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<tr>
<td>Practical: 2 Hr/week</td>
<td>Oral: 25 Marks</td>
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<td>Term work: 25 Marks</td>
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### Unit 1: Introduction

Introduction to embedded system, Definition of embedded systems, characteristics of embedded systems, design metrics, applications, embedded system operations, Software development, software architectures, IDE, Communication protocols: Blue tooth, Zigbee, etc.

### Unit 2: Embedded Processor

Processor internal architecture, processor technology, processor types, embedded processor, RISC design philosophy, specifications of processor/s, memory organization, memory interfacing aspects, processor & memory selection for different applications, Interrupts.

### Unit 3: Arm Processor

ARM-7 processor LPC 2148 architecture, data flow model, memory organization, programming model, ISP & IAP, register banking, operating modes: ARM mode & Thumb mode, I/O port read/write operations, Status registers

### Unit 4: Programming

On chip communication protocols of LPC 2148, Programming concepts using embedded C, Programming ARM processor for implementing various communication protocols, keyboard Interface, LCD interface, on chip ADC/DAC interface.

### Unit 5: Real Time Operating Systems Concept

Real Time Operating Systems µCOS-II, Features, State diagram, comparison with traditional OS, Semaphore, shared data problem, scheduling algorithms, dead lock.
Unit 6: Case Study

Priority inversion problem, priority inheritance, interrupts management; inter task communication, memory management, Time delays. Case studies: Cruise control, digital Camera.

Text Books

1. Rajkamal _ Embedded Systems _ TMH.
2. David Simon _ Embedded systems software primer_ pearson
3. Andrew sloss _ Arm System Developer guide_
4. Christopher Hallinan _ Embedded linux primer_ Prenice Hall

References Books

1. Frank Vahid, _ Embedded sytem design_ , PHI
2. Steve Furber _Arm System on chip architecture_, AddisonWesely
3. Alessandro Rubini and Jonathan Corbet, _LinuxDevice Drivers_, 3rd Edition O_Reilly

List of Experiments

1. Write C code for implementation of four tasks using µCOS-II RTOS on LPC 2148 [ARM-7] processor.
2. Write C code for use of semaphore using µCOS-II RTOS on LPC 2148 [ARM-7] processor.
3. Write C code for message queue implementation using µCOS-II RTOS on LPC 2148 [ARM-7] processor.
4. Write C code for mail box implementation using µCOS-II RTOS on LPC 2148 [ARM-7] processor.
Elective-I
ADVANCED MEASUREMENT SYSTEMS (404204)

Teaching Scheme

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<th>Lectures/week: 4 Hrs</th>
<th>Examination Scheme</th>
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<tr>
<td>Practical/week: 2Hrs</td>
<td>Paper: 100 Marks</td>
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<td>Practical: 50 Marks</td>
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<td>Term work: 25 Marks</td>
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Unit 1: Signal Integrity:

Signal Integrity design Issues, Signal Integrity Testing Challenges and solutions, Electrical Validation and Debug with DPO/MSO Series Oscilloscopes and Arbitrary Waveform Generators

Unit 2: Hardware design and testing methods:

Logic analyzer, its architecture & operation and Use of logic analyzer, Spectrum analyzer Network analyzer, Oscilloscope, DSO trigger modes Examples using MSO Use & limitations of different types of analysis

Unit 3: Role of electronic measurements in Embedded Systems:

Design issues and role of electronic measurements for debugging in Automotive Electronics (ECU), Serial bus decode Test instruments for a variety of standards, including: USB, PCI Express, CAN/, I2C, Need of interfacing, interfacing techniques, interfacing of different displays including Graphic LCD (320X240), interfacing of input devices including touch screen etc, interfacing of output devices like thermal printer etc., embedded communication using CAN and Ethernet, RF modules, GSM modem for AT command study etc.

Unit 4: Microwave Measurements:

Unit 5: Virtual Instrumentation:

Virtual Instrumentation, VISA (GPIB, VXI, PXI), SCPI coding. Test system development using Virtual Instrumentation, Software role in virtual Instrumentation, Hardware role in virtual instrumentation. Virtual Instrumentation and its application, modulation techniques: TDM, FDM, ASK, PSK, application of the same in instrumentation, Distortion analyzer, Logic analyzers. Case study of Lab View based Data acquisition system design.

Unit 6: Digital Instrumentation:

Universal counter and its mode _ totalizing, frequency, period, time interval, ratio, measurement errors, application of counters for frequency meter, capacitance meter and timers, automation in digital instruments, ADC and DAC techniques, types, and their specifications, V to F converter, Sample and hold, analog multiplexer, data loggers.

Text Books:

1. Electronics Instrumentation by H.S.Kalsi
2. Microwave circuits and design by Samuel Liao
3. Handbook for I2C and CAN protocol
4. Process Handbook by Liptac

References:

1. Application Notes by Tekteronics
2. Application Notes by Agilent
3. Hand Book for Electronic Measurements by Coombs

List of Practicals

(Equipments Required: DSO, MSO, Logic Analyzer , Power Scope, Arbitrary signal generator)
1. Study and application of Universal counters
2. Study of DSO _ measurement of response time of relay using DSO
3. Study and application of ADC 0809
4. Study and application of DAC 0808
5. Study of Arbitrary waveform generator
6. Program to demonstrate I2C Protocol.
7. Program to demonstrate CAN Protocol.
8. System building and simulation on Virtual Instrumentation
9. VSWR Measurement (Using Vmax / Vmin Method)
ADVANCED POWER ELECTRONICS (404204)

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Unit 1: **Modern Rectifiers and Converters**

Single phase series converters, 12 pulse converters. Three phase IGBT based PWM rectifier, analysis, comparison with SCR based conventional converters with respect to harmonic content, P.F. Power factor conditioning of diode rectifiers EM I and Line Power quality problems of thyristor converters Double sided PWM converter systems

Unit 2: **Motor Controls**


Unit 3: **Multilevel Inverters**


Unit 4: **Resonant converters and Power supplies**

Concept of Soft switching, Resonant converter Analysis and Design. SLR, PLR, ZCS, ZVS

Converter analysis and Design. Synchronous rectifiers, Low Dropout Regulators, Hot Swappable

Redundant Power Supplies, Design of High Frequency magnetic components for converters

Bi-Directional Power Supplies

Unit 5: **Renewable Energy Sources**

Role of Power Electronics in renewable energy, variable wind energy conversion system with DC to DC converters followed by 3Phase VSI, Photovoltaic energy conversion system, Solar Battery powered drives, traction drives, energy conversion in electrical drives, Battery Chargers.
Unit 6: **Power Conditioning**

Power quality, power line disturbances and its remedies, energy audit, solar power conditioning.

Power transmission. FACTS Flexible AC Transmission, HVDC

**Text books**

1) Modern power electronics by P.C.Sen --S Chand & co .
4) Fundamentals of electrical drives by G.K.Dubey.

**Reference books**

1)Thyrister DC drives by P.C sen,Jhon Wiely.
2) Modern power electronics & AC drives –Bimal K Bose Pearson publication.

**List of Practical’s (Min 06 Practicals+2 simulations Practicals)**

1. 1Ph/3Ph IGBT based PWM switched rectifier.
2. Feedback Controlled Dc Motor Drive
3. V/F controlled AC induction motor drive
4. Three phase 4-Quadrant reversible Flux vector control for induction motor.
5. Study of Advanced Modulation techniques any one.
6. Low dropout regulator
7. 3 phase brushless generator for wind energy applications.
8. 4-Quadrant reversible DC drive chopper.
9. Speed Control of Universal Motor.
10. Simulation of closed loop controlled DC drive using PSIM/Matlab/MathCad
11. Simulation of Multilevel Inverter
12. Simulation of three phase dual converter drive using Psim/Matlab/MathCad
13. Simulation of Closed loop controlled AC motor drive using PSIM / Matlab/MathCad
14. Case Study/ Industrial Visit
BIO-MEDICAL INSTRUMENTATION (404204)

Teaching Scheme

Lectures/Week: 4 Hrs

Practical/Week: 2 Hrs.

Unit 1: Introduction


Unit 2: Nervous System-Anatomy

Introduction to Nervous System-Anatomy, 10-20 electrode for EEG, Evoked- Potential, Types and significance of EEG Signals (±, ², ³, , ) EEG machine, Analysis of Diseases using EEG, EMG Machine.

Unit 3: Heart System


Unit 4: Medical Instruments and Measurement


Unit 5: Clinical Lab Instruments


Unit 6: Radiology Instrumentation
Introduction to Radiology Instrumentation such as X-Ray Machine, Computer Tomography (CT Scan), MRI Machine, Introduction to Ultrasonic Doppler Machine, Lasers in Medicine- Vision Correction, Dermatological. Principles, types, applications Dermatology, vision correction information to be collected from net ASL (American Society for lasers), Medical laser book: Selection of lasers type, powers, wavelength, Depth of penetration, Ximer Laser. Introduction to Dental Instruments on following topics: Aerator, Amalgamator, Shadow Less Light, Ortho Pentamo Graph.

Text Books

1. Carr and Brown, _Biomedical Instrumentation_.
2. Cromwell, _Biomedical Instrumentation and Measurement_, PHI.

Reference Books

1. Webster, _Application and Design of Medical Instruments"_.
2. R. S. Khandpur, _Biomedical Instrumentation_.

List of Practical / Assignments

Students are expected to perform Minimum 8 practical from the list mentioned below.
1. To study ECG Machine (Single channel or Multichannel).
2. PC based ECG machine _ waveform simulation C based
   OR
   ECG waveform generation using microcontroller.
3. Interface of PC simulated waveform with ECG machine
4. To study and measure Blood Pressure using sphygmomanometer/ Digital BP Instrument
5. To study Defibrillator/pacemaker
6. Measure body temperature using Digital Clinical Thermometer
7. ECG \ QRS Detector + Counter to display heart rate
8. To study and measure pulse rate using finger plethesmography.
9. measurement of concentration using spectrophotometer
10. To study EEG/EMG Machine.
11. To study Blood cell counter.
12. Study of Bedside Monitor, Drip Rate Monitor (ICU Monitor)
13. Study of Dialysis System
15. Study of Laser Treatments in Medicines.
MECHATRONICS (404204)

Teaching Scheme | Examination Scheme
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Lectures/Week: 4Hrs | Paper: 100 Marks
Practical/Week: 2Hrs. | Practical: 50 Marks

Term work: 25 Marks

Unit 1: Overview of Mechatronics

Key Elements, Mechatronics Design Approach, Functions of Mechatronics system, Division of functions between Mechanics and Electronics, Stepwise Design Procedure, Modelling Procedure. Mechanical Components and systems: Bearings and Bushings, Belts and Pulleys, Brakes and clutches, Chains and Sprockets, Couplings and joints, gears, Pulleys and Belts, Solenoids, springs, Switches

Unit 2: Physical System Modelling

Modelling Electromechanical Systems, Simple dynamic models, Elastic system modelling, Structures and Materials, Common structures in Mechatronics: Beams, Springs, Thin Plates, Vibration and analysis,

Unit 3: Sensors and actuators Sensors and transducers:

Principle of operation, Specifications, Selection criterion (Design aspects) for Force, pressure, temperature, motion (Rotary and Linear), position, flow, Level Sensors

Actuators: Classification, Specifications, Selection criterion and application areas for: Mechanical actuators: Pneumatic, Hydraulic, programmable electro hydraulic valves Electrical actuators: Relays and Solenoids, Stepper Motors, DC brushed motors, DC brushless motors, DC servo motors, 4-quadrant servo drives, PWM_s, Pulse Width Modulation, Variable Frequency Drives, Vector Drives, Drive System load calculation

Unit 4: Systems and Control

Role of controls in Mechatronics, Key elements of controlled Mechatronics system, Integrated Modelling, design and control implementation, Case study: Design of a mobile Robot, Modern examples of Mechatronics systems in action, Special Requirements of Mechatronics that Differentiate from _Classic_ Systems and Control Design, State space analysis, Design optimisation of Mechatronics system, controller examples

Unit 5: Computers and Logic Systems

The Mechatronics use of computers, concept of real time, System interfaces, Terminology and Definitions (Serial vs. Parallel, Bit Rate vs. Baud Rate, Synchronous

Unit 6: Software and Data Acquisition

Data logging functional requirement: Acquisition, Sensors, Signal Connectivity, Signal Conditioning, Conversion, Online Analysis, Logging and Storage, Offline Analysis, Display, Report Generation, Data Sharing and Publishing; Data-Logging Systems
Different applications of Mechatronics as Case study

Text Books


Reference Books

2. Christopher Kilian, _Modern control technology_, 2nd edition, Thomson

List of Practicals

1. Study of various mechanical components used in Mechatronics applications
2. Study of different switches and relays with their applications in Mechatronics
3. Study of Flip Flops, Timers, ADC, DAC and Op _Amp circuits as Mechatronics system elements
4. Calibration of flow meters/Thermocouple/RTD
5. Study of Load Cells for Mechatronics applications
6. Vibration measurement/Displacement measurement/level measurement
7. Verification of P, P+I, P+D, P+I+D control actions
8. Study of linear conveyor control system
9. Study of rotary table positioning systems
10. Analysis of control system using software like MATLAB/SIMULINK or equivalent.
11. Development of ladder diagram/programming PLC for level control, position control or any other mechanical engineering application
12. Study of Data acquisition system
13. Study of Microcontrollers applications for Mechatronics systems
14. Case study of any Mechatronics system

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Elective-II

ADVANCED COMPUTER ARCHITECTURE (404205)

Teaching Scheme                                           Examination Scheme

Lectures/Week: 4Hrs                                         Paper: 100 Marks

Unit 1: Overview of Parallel Processing and Pipelining Processing


Unit 2: Pipeline Architecture

Principles and implementation of Pipelining, Classification of pipelining processors, General pipelining reservation table, Design aspect of Arithmetic and Instruction pipelining, Pipelining hazards and resolving techniques, Data buffering techniques, Job sequencing and Collision, Advanced pipelining techniques, loop unrolling techniques, out of order execution, software scheduling, trace scheduling, Predicated execution, Speculative loading, Register Stack Engine, Software pipelining, VLIW (Very Long Instruction Word) processor, Superscalar Architecture- Pentium, Ultra SPARC, Explicitly Parallel Instruction Computing (EPIC) Architecture, Case study of Intel Itanium Processor

Unit 3: Vector and Array Processor


Unit 4: SIMD Computer Organization

Masking and Data network mechanism, Inter PE Communication, Interconnection networks of SIMD, Static Vs Dynamic network, cube hyper cube and Mesh Interconnection network. Parallel Algorithms for Array Processors: Matrix Multiplication, Sorting, FFT

Unit 5: Multiprocessor Architecture:
Loosely and Tightly coupled multiprocessors, Processor characteristics of multiprocessors, Inter Processor communication network, Time shared bus, Crossbar switch, Multiport Memory Model, Memory contention and arbitration techniques, Cache coherency and bus snooping, Massively Parallel Processors (MPP), COW_s and NOW_s Cluster and Network of Work Stations, Chip Multiprocessing (CMP), Inter Processor Communication and Synchronization, Case Study of IBM Power4 Processor

**Unit 6: Parallel Programming and Multithreaded Architecture**

Parallel Programming Techniques: Message passing program development, Synchronous and asynchronous message passing, Message passing parallel programming, Shared Memory Programming, Data Parallel Programming Multithreaded Architecture: Multithreaded processors, Latency hiding techniques, Principles of multithreading, Issues and solutions, case study-JVM

**Textbooks**


**References**

1. V.Rajaraman, L Sivaram Murthy, _Parallel Computers_, PHI.
3. Kai Hwang, _Scalable Parallel Computing_
4. Richard Y. Kain, _Advanced Computer Architecture_
### ENTREPRENEURSHIP AND BUSINESS PLANNING (404205)

<table>
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<th>Teaching Scheme</th>
<th>Examination Scheme</th>
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<td><strong>Lectures/Week:</strong> 4Hrs</td>
<td><strong>Paper:</strong> 100 Marks</td>
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#### Unit 1: Introduction

Entrepreneur: Present and Past, Entrepreneurship for an Engineer, Identify Business Opportunities and Set Goals. Entrepreneurs Skills: Communication Skills, Math Skills, Problem-Solving Skills

#### Unit 2: Entrepreneurs in a Market Economy and Ownership

Entrepreneurs in a Market Economy: An Economy, The Concept of Cost, Government in a Market Economy. Select a Type of Ownership: Run an Existing Business, Own a Franchise or start a Business, Choose the legal form business

#### Unit 3: Business Plan


#### Unit 4: Hire and Manage a Staff: Record keeping and Accounting

Hire and Manage a Staff: Hire Employees, Create a compensation package, Manage staff , Record Keeping and Accounting: Set up a record keeping system, Understand basic accounting, Tracking inventory

#### Unit 5: Financial Management, Use Technology

Financial Management: Manage cash flow, Analyze financial performance, Hire experts, Use of Technology: Technology and business, Learning about the internet, Purchase technology
Unit 6: Meeting Legal, Ethical, and Social Obligation Growth in Today’s Marketplace

Meeting Legal, Ethical, and Social Obligation: Understanding legal requirements, Ethical issues in business, meeting social responsibilities. Growth in Today’s Marketplace: Developing a strategy for growth, Global Trends and opportunities, Culture and business

Text books

ROBOTICS AND AUTOMATION (404205)

Teaching Scheme                               Examination Scheme
Lectures: 4 Hrs/week                          Paper: 100 Marks

Unit 1: Introduction to Robotics and Automation


Unit 2: Kinematics and Dynamics

**Kinematics:** Homogeneous co-ordinate vector operations, matrix operations, co-ordinate reference frames, Homogeneous transformation and manipulator orientation relative points reference frames, Workspace, Forward Kinematics - forward solutions- Link co-ordinate frames, D-H matrix, Inverse Kinematics - Existence and Uniqueness of Solutions, Analytical Approaches - Reduction of Inverse Kinematics to Sub-problems, Pieper’s Solution, Numerical Techniques, techniques of using direct & geometric approach

**Dynamics:** Newton's equation, Euler equations, Dynamic Modeling of Robotic Manipulators - Force and Torque Balance on an Isolated Link, Two-Link Robot Example, Closed-Form Equations. Kane’s Method in Robotics - Two DOF Planar Robot with Two Revolute Joints, Generalized Coordinates and Speeds, Velocities, Partial Velocities, Accelerations, Generalized Inertia Forces, Generalized Active Forces, Equations of Motion, Special Issues in Kane’s Method

Unit 3: Mechanisms Actuators and Sensors

Unit 4: Motion Planning and Motion Control

On-off trajectory, relocking and acceleration profile, Cartesian motion of manipulator, joint interpolated control, Jacobian in terms of D-H matrix, Trajectory Planning: Command 2 Generation, Pick-and-Place Operations, Obstacle avoidance, Basic control system, control loops of robotic system, Feed-forward Control Action, Fuzzy controllers, Error Budgeting.

Unit 5: Robot Vision Systems


Unit 6: Automation


Text Books

Reference Books
## SYSTEM ON CHIP (404205)

<table>
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<th>Teaching Scheme</th>
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<td>Lectures: 4 Hrs/week</td>
<td>Paper: 100 Marks</td>
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### Unit 1: Introduction to MEMS and SoC

Mechanical Transducers: transduction methods, accelerometers, gyroscopes, pressure sensors, MEMS microphones, mechanical structures, actuators.

### Unit 2: Control and Materials of MEMS

Controls of MEMS: Analog control of MEMS, Sliding mode control of MEMS, Digital control of MEMS, Materials for MEMS: Substrate and wafers, Active substrate material, silicon, Silicon compound, Silicon pezoresisters, Gallium arsenide, Quartz, piezoelectric crystals, Polymers.

### Unit 3: Transducers

Chemical and Biological Transducers: basic concepts of cellular biology, chemical sensors, molecule-based biosensors, cell-based biosensors, chemical actuators, biological transducers, electrophoresis: optical transducers, thermal transducers, magnetic transducers, RF transducers.

### Unit 4: Introduction to SOC

Design of system on chip, Microsystems technology and applications, core architecture for digital media and the associated compilation techniques

### Unit 5: Overview of Physical Design Automation

Physical design automation, behavioural synthesis, synthesis of FPGAs and testable ASICs micromachining processes: substrates, lithography, wet/dry etching processes, deposition processes, film stress, exotic process
Unit 6: SOC Testing and Packaging

Hardware/software co-design, test and design of circuit to integrated systems, testable design and testing of Microsystems, embedded core based system on chip test strategies Micro System Packaging: Over view of mechanical packaging of micro electronics micro system packaging

Text Books:


Reference Books:

Project Part I (404206)

Teaching Scheme | Examination Scheme
------------------|----------------------
Tutorial: 2 Hrs/Week | Term Work: 50 Marks

Note:

1. Term work assessment is based on the project topic. It consists of Literature Survey and basic project work. The abstract of the project should be submitted before Term work assessment.
2. The report consists of the Literature Survey basic project work and the size of the report should be maximum of 40 pages.
3. The examination is conducted by two examiners (internal and external) appointed by the university. The examiners appointed must have minimum 6 years of experience with UG qualification and 3 years with PG qualification.
4. The assessment is based on Innovative Idea, Depth of understanding, Applications, Individual contributions, and presentation, and the grade given by the internal guide, which is based on the work carried out in a semester.
5. A certified copy of report is required to be presented to external examiner at the time of final examination.
TERM-II

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<td>PROJECT (PART-II)***</td>
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**Elective-III**

1. Audio and Video Engineering
2. Image Processing and Machine Vision
3. Optical and Microwave Communication
4. Soft Computing tools

**Elective-IV**

1. Advanced Communication System
2. Automotive Electronic Systems
3. Artificial Intelligence
4. Nanotechnology in Electronics
5. Any one subject from the list of Elective IV of Computer/IT/Electrical/Instrumentation
OR institute can offer an elective-IV based on any industry need with prior approval of BOS (Electronics)

**Note:**

1) All Theory papers are three hours duration
2) Practical/Oral shall be based on term-work
3) Term-work of Project Part I consist of project report based on project
4) ** ** Exam at the end of II term
## SEM-II

**COMPUTER NETWORK AND SECURITY (404207)**

<table>
<thead>
<tr>
<th>Teaching Scheme</th>
<th>Examination Scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Lectures:</strong> 4 Hrs/week</td>
<td><strong>Paper:</strong> 100 Marks</td>
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<tr>
<td><strong>Practical:</strong> 2Hrs/week</td>
<td><strong>Oral:</strong> 50 Marks</td>
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</table>

### Unit 1: Introduction to Computer Networks


### Unit 2: Application Layer

Application layer protocols and applications like Ping, FTP, telnet, http (www), SMTP, SNMP, Trace route, TFTP, BOOTP, DNS, NFS, RPC, X-server, E-mail, Introduction to streaming Audio/Video,P2P file sharing, Introduction to socket programming, Introduction to HTML programming.

### Unit 3: Transport Layer and Network Layer


### Unit 4: Data link layer

Data link layer: Framing, Flow & Error control Protocols, noiseless channels, Noisy channels, HDLC, PPP, Multiple access techniques-random access, controlled access & Channelization, Ethernet types-bridged, Switched, Full duplex, Fast & gigabit Ethernet. Introduction to Data link layer in 802.11 LAN, Connecting devices like passive hubs, repeaters, Active hubs, Bridges, Two-layer Switches, Routers, three layer switches, Gateway etc., Backbone networks, Virtual LANs.
Unit 5: Physical Layer

Physical layer - Data rate limits, Transmission media-guided and Unguided, Switching systems - Circuit switching, Datagram Switching & Virtual circuit switching. Structure of circuit and packet switch, cable modem and DSL technologies, SONET basics, Communication satellites (LEO/MEO/GEO), Introduction to physical layer in 802.11 LAN & 802.15 WPAN.

Unit 6: Basics of Network Security and Network administration.


Text Books


Reference Books

1. William Stallings, _computer Networks and Cryptography_, 3rd edition, Pearson Education
8. Nurul Sarkar, _Computer Networking & Hardware concepts_, Information Science Publisher, USA.

List of the Experiments

1. Implementation of LAN using star topology and connectivity between two computers using cross over UTP CAT5 cable.
2. Installation and configuration of Web Server and hosting web page using HTML programming.
3. Installation and configuration of Proxy Server.
4. Installation and configuration of FTP server for FTP communication.
5. Installation and configuration of Telnet server for Telnet communication.
6. Write a program in _c_ for Encryption and Decryption
7. Write a program in _c_ for Shortest Path algorithm.
8. Connectivity of LAN computers to Internet using Dial-Up modem/leased line modem/Mobile Handset. (Installation and configuration).
9. Installation of Suitable Network Monitoring software and Analysis of Intranet activities.
10. Installation of Suitable Protocol Analyzing software and Analysis of Intranet activities.
11. Assignment of LAN or WAN simulation using Suitable network Simulating software.
PROCESS AUTOMATION (404208)

Teaching Scheme

Lectures/Week: 4Hrs
Practical/Week: 2Hrs.

Examination Scheme

Paper: 100 Marks
Oral: 50 Marks

Term work: 25 Marks

Unit 1: Introduction

Block Diagram of process control system, process control principles, Instrumentation standard signals.

Unit 2: Controllers

Closed loop control system, process characteristics, control system parameters, degree of freedom. Controller modes: Discontinuous, continuous and composite controller modes. Electronic controllers: Realization of controllers using operational amplifiers circuits. Pneumatic controllers, Hydraulic controllers, Tuning of PID controller.

Unit 3: Control Elements

Comparison of electronic, pneumatic, hydraulic systems. Control Valves: Principle of control valve, different parts of control valve, flow characteristics, selection of control valve, control valve sizing, different types of control valve and their applications, control valve noise, valve positioner, cavitations and flashing.

Unit 4: Advanced process control

Feedback and feed forward control systems, cascade, ratio, selective and adaptive control systems, applications of these systems in process industry. Statistical process control, self tuning controller. Internal Model Control [IMC], Model Predictive control [MPC], Process Model Based Control [PMBC].

Unit 5:

Instrumentation schemes for boiler, Heat exchanger, Distillation column control, Evaporator, Compressor. Introduction to Robotics: Definition, Need of robot, Robot components, Classification and applications.
Unit 6: Computers in Instrumentation

Supervisory control systems, direct digital control systems, Distributed control systems, SCADA. Auxiliary process components: Square root extractor, flow tantalizer, recorders, alarm annunciators, Control Panels.

Text Books

2. C D Johnson, _Process Control Instrumentation Technology_, PHI

Reference Books

1. Andrews, _Applied Instrumentation in Process Industries_
2. Considine, _Process Instrumentation and Control Handbook_

List of Practicals

1. Calibration of electro pneumatic converter.
2. Tuning of PID controller for different control actions.
3. To plot the control valve characteristics.
4. Study of Recorders.
5. Communication with SMART TX.
6. Microcontroller based instrumentation system.
7. Any one experiment based on simulation of instrumentation system/Process control.
## Elective-III

**AUDIO AND VIDEO ENGINEERING (404209)**

<table>
<thead>
<tr>
<th>Teaching Scheme</th>
<th>Examination Scheme</th>
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<tbody>
<tr>
<td><strong>Lectures/week:</strong> 4 Hrs</td>
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<tr>
<td><strong>Practical/week:</strong> 2 Hrs</td>
<td><strong>Practical:</strong> 50 Marks</td>
</tr>
<tr>
<td><strong>Term work:</strong> 25 Marks</td>
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### Unit 1: Fundamentals of Television and Displays

Color TV systems, Television basics, color fundamentals, mixing of colors, color perception, chromaticity diagram, Digital TV cameras. Display devices: LCD, TFT, LED, Plasma, and HDTV.

### Unit 2: Television Standards

NTSC, PAL, SECAM systems, color TV transmitter, high level, low level transmitters, color TV receivers, remote control, antennas for transmission. TV alignment and fault finding with Wobbuloscope and TV pattern generation, field strength meter.

### Unit 3: Digital TV


### Unit 4: HDTV Systems

HDTV standards and systems, HDTV transmitter and receiver/encoder, satellite TV, video on demand, CCTV, CATV, direct to home TV, set top box with recording facility, conditional access system (CAS), 3D TV Systems, Digital broadcasting, case study (Cricket match, Marathon, Football match).

### Unit 5: Audio Systems

Overview of Digital Sound Recording, Digital sound recording, CD recording, CD/DVD player, MP3 player, Blue Ray DVD Players, Audio Compression Standards, MPEG,MP3, Audio Compression ITU-T Standards (G.)
Unit 6: Acoustics

Studio Acoustics, reverberation, PA system for auditorium, Acoustic chamber, Graphic Equalizers and Digital Filters, chord less microphone systems, special type of speakers, Satellite radio reception.

Text Books


Reference Books

1. S. P. Bali, _Color TV Theory and Practice_
2. Bernard Grobb, Charles E, _Basic TV and Video Systems_
3. Gulathi, _Monochrome & Color TV_

List of Practical Assignments

1. Voltage and waveform analysis for color TV.
2. Alignment and fault finding for color TV.
4. Study of direct to home TV and set top box.
5. Simulation of video compressing techniques.(Software Assignments)
6. Practical visit to TV transmitter/studio.
7. Study of PA system with chord less microphone.
8. Study of Audio system: CD players and MP3 player.
9. Study of HDTV.
10. Study of Digital TV.
IMAGE PROCESSING AND MACHINE VISION (404209)

Teaching Scheme

Lectures/week: 4 Hrs  
Practical/week: 2 Hrs

Examination Scheme

Paper: 100 Marks  
Practical: 50 Marks

Term work: 25 Marks

Unit 1: Introduction to image processing

Image Processing Applications, Fundamental steps in Digital image processing, Elements of visual perception, Image sensing and acquisition, Basic Concepts in Sampling and Quantization, representing digital images.

Unit 2: Image Enhancement


Unit 3: Image Segmentation

Some Basic Relationships between pixels, point, line and edge detection, Gradient operators, Canny edge detection, pyramid edge detection. Edge linking and boundary detection, Hough transform, Chain codes, boundary segments, skeletons, Boundary descriptors, Fourier descriptors. Thresholding, global thresholding, adaptive thresholding, use of boundary characteristics for histogram improvement and local thresholding, Region based segmentation, Region growing, region splitting and merging.

Unit 4: Image Compression

Data redundancies Elements of information, variable-length coding, predictive coding, Transform coding, Image compression standards, Wavelets and Multiresolution processing Image pyramids, subband coding.

Unit 5: Representation and Description

Shape Representation and Description, Region Identification, Contour based shape representation and description, Region based representation and description, Shape classes
Unit 6: Object Recognition and 3D vision

Statistical pattern recognition and Syntactic pattern recognition, Graph Matching, 3D vision and Geometry, Single perspective camera, scene reconstruction from multiple views.

Text Books


Reference Books


List of Practical Assignments

1. Study of different File Formats BMP, TIFF
2. Study of statistical Properties: mean standard Deviation, Profile, Variance and Histogram Plotting
3. Histogram Equalization and modification
4. Gray Level transformation: contrast Stretching negative, power law transformation
5. Spatial Domain Filtering: Smoothing and sharpening Filters
6. DCT/IDCT of given Image
7. Shape Description and Object Recognition
8. Assignment on 3D vision
OPTICAL AND MICROWAVE COMMUNICATION (404209)

Teaching Scheme

Lectures/Week: 4Hrs
Practical/Week: 2Hrs.

Examination Scheme

Paper: 100 Marks
Practical: 50 Marks

Term work: 25 Marks

Unit 1: Fundamentals of FOC

Basic block diagram of Optical Fiber Communication system, Principles of light propagation through a fibre, Different types of fibres and their characteristics, Connectors & splicers, Fibre termination, Optical sources and Optical detectors. Fibre optic sensors, Different types of modulators

Unit 2: Dispersion, Modulation and Optical Networks


Unit 3: Industrial and Medical Applications


Unit 4: Microwave Devices and Components

Basic concepts and properties of wave guides, Scattering Matrix of Microwave passive Network and Smith Chart, Properties of S matrix, S matrix formulation of two-port junction, T junctions- H plane, E plane and EH plane Tee junctions, its S matrix and properties, Applications of Hybrid junction. Directional coupler, Gyrator, Isolator, Circulator, Phase changer, Attenuator and matched terminations

Unit 5: High Power Microwave Sources

Two cavity klystron amplifier, Mechanism and mode of operation, Power output and efficiency, Mode curve, Equivalent circuit and voltage gain, Beam loading, Applications, Multi cavity Klystron, Reflex Klystron, Traveling Wave Tube and Magnetron - Principle
of Mechanism and mode of operation, Power output, Efficiency, Mode curve, Equivalent circuit

**Unit 6: Microwave Solid State Devices and Circuits**

Crystal diode, Schottky diode, PIN diode, IMPATT diodes, Mechanism of operation, Application as oscillator and amplifier, Tunnel diode as oscillator / amplifiers, Varactor diode, Parametric amplifier, Unipolar and bipolar Microwave transistors, Gunn diode, Mode of operation, Gunn diode as an Oscillator and an amplifier, Terrestrial and satellite based microwave communication systems

**Text Books**


**Reference Books**

1. Monte Ross, _Laser Applications_, McGraw Hill
2. Samuel Liao, _Microwave devices and circuit_, PHI.

**Practicals**

1. V-I & I-P characteristics of LED.
4. Study of transmission of analog / Digital signals through Fiber optic Cable.
9. VSWR, isolation and insertion measurement of Isolators and Circulators
10. S-parameter and VSWR measurements of Tees
SOFT COMPUTING TOOLS (404209)

Teaching Scheme

Lectures/week: 4 Hrs
Practical/week: 2 Hrs

Examination Scheme

Paper: 100 Marks
Practical: 50 Marks

Term work: 25 Marks

Unit 1: Introduction to Neuro-Fuzzy and soft computing


Unit 2: Fuzzy Set Theory

Fuzzy logic, Fuzzy sets, Fuzzy set operations, Fuzzy rules, Fuzzy algorithms, the fuzzy algorithm with linear constituents, determining the fuzzy algorithm

Unit 3: Fuzzy Control

Systematic approach for the design of fuzzy control system, Synthesis and validation of a fuzzy controller, determining the control laws, determining the fuzzy controller, validating the fuzzy controller

Unit 4: Artificial Neural Network

Artificial neural network theory, Topologies, Multilayer perceptron, unsupervising neural network, Radial basis function, Learning algorithm, Numerical Examples regarding MLP_s and RBF.

Unit 5: Neural Network Application

Neural network applications for identifying non-linear dynamic system and for complex system control, Image processing, and communication.

Unit 6: Neuro Fuzzy Modelling

Introduction, ANFIS architecture, Hybrid learning algorithm Learning methods that cross-fertilize ANFIS and RBFN, Use of ANN for process control.
Text Books


Reference Books

2. S. N. Sivanandam, S. N. Deepa, _Principals of soft Computing_, Wiley India.

List of practicals

1. Design and implement ANN to compute OR, AND, NOT gate for the two input using MP model.
2. Implement perceptron algorithm for solving EX-OR problem.
3. Implement Back Propogation algorithm to solve classification problem.
4. Implementation of various learning learning laws
5. Implement Kohonen algorithm for character recognition
6. Implement various membership functions
7. Implement primary and composite linguistic fuzzy variables
8. Implementation of defuzzification using various method
9. Implement fuzzy controller to control simple process (Mamdani / Sugeno / Tsukamoto)
Elective-IV

ADVANCED COMMUNICATION SYSTEMS (404210)

Teaching Scheme                                      Examination Scheme

Lectures/week: 4 Hrs                                Paper: 100 Marks

Unit 1: Introduction to Cellular Mobile System

Uniqueness of Cellular Mobile Environment, Severe Fading, Model of Transmission Medium, delay spread & coherence bandwidth, direct wave path, line of sight path & obstructive path, Operations of cellular systems, Max. no. of frequency channels per cell, frequency reuse schemes, co channel interference reduction factor, handoff mechanism, cell splitting Cell coverage for signal and traffic, Ground incident angle, elevation angle, reflection angle & reflection point, mobile point to point Lee model, phase difference between direct path & a ground reflected path, Formula for mobile radio propagation over water, flat open area, between fixed stations, land to mobile over water, foliage loss, 1 mile intercept near field, long distance propagation, Finding antenna height gain in obstructive/ non obstructive condition, Point to point model cell sight antenna heights & signal coverage cell

Unit 2: Cell Site Antennas and Mobile Antennas

Free space path loss formula, EIRP, gain & pattern relationship, Relationship between gain & beam width, antennas at cell site, interference reducing directional antennas, space diversity antennas, mobile antennas Co-channel and non-co channel interference, Design of Omni directional & directional antenna system, lowering antenna height, neighboring channel interference, near end far end interference, Effect on coverage & interference by decreasing power, antenna height & beam tilting, interference between systems, UHF-TV interference, long distance interference, Frequency Management & channel assignment Numbering the channels, grouping into subsets, frequency spectrum utilization, set up channels, access channel spacing channels, self location schemes, autonomous registration, fixed/non fixed channel assignment, channel sharing & borrowing, sectorisation, underlay-overlay, Handoffs & dropped calls, Two types of handoffs, initiation of a handoff, delaying handoff, forced handoff, power difference handoffs, mobile assisted & soft handoffs, intersystem handoffs

Unit 3: Operational Techniques

Adjusting parameters of a system, increasing coverage, transmitted power, cell site antenna height, received signal strength threshold, diversity receiver, reducing interference, increasing traffic capacity, diversity, cell splitting techniques, microcells, narrow beam concept, separation between highway cell sites, Digital Cellular systems GSM, system architecture, radio interface, protocols, handover, security, authentication, HSCSD, GPRS
Unit 4: Introduction to Satellite Communication Systems

Satellite Communication overview, Orbital Mechanics, Look Angles, Attitude and Orbit Control System, Telemetry; Tracking Commands and Monitoring System, Power System, Communication systems, Transponders, Different types of Antennas and relationships, Antennas used in practical systems.

Unit 5:


Unit 6: Multiple Access and VSAT Systems

FDMA, TDMA and CDMA Multiple Access Systems, VSAT Systems, Network Architectures, VSAT-Earth Stations Engineering,

Text Books

ARTIFICIAL INTELLIGENCE (404210)

Teaching Scheme                                     Examination Scheme

Lectures/week: 4 Hrs                                 Paper: 100 Marks

Unit 1: Foundation


Unit 2: Searching

Search and exploration, Informed search strategies, heuristic function, local search algorithms and optimistic problems, local search in continuous spaces, online search agents and unknown environments, Constraint satisfaction problems (CSP), Backtracking search and Local search for CSP, Structure of problems, Adversarial Search, Games, Optimal decisions in games, Alpha- Beta Pruning, imperfect real-time decision, games that include an element of chance.

Unit 3: Knowledge Representation

First order logic, representation revisited, Syntax and semantics for first order logic, Using first order logic, Knowledge engineering in first order logic, Inference in First order logic, prepositional versus first order logic, unification and lifting, forward chaining, backward chaining-Resolution, Knowledge representation, Ontological Engineering, Categories and objects, Actions-Simulation and events, Mental events and mental objects

Unit 4: Learning

Learning from observations, forms of learning, Inductive learning, Learning decision trees, Ensemble learning, Knowledge in learning, Logical formulation of learning, Explanation based learning, Learning using relevant information, Inductive logic programming, Statistical learning methods, Learning with complete data, Learning with hidden variable, EM algorithm, Instance based learning, Neural networks - Reinforcement learning, Passive reinforcement learning, Active reinforcement learning, Generalization in reinforcement learning.

Unit 5: Perception and Expert System

Visual perception-Waltz_s algorithm, Introduction to Expert System, Architecture and functionality, Example Expert system
Unit 6: Natural Language Understanding

Why NL, Formal grammar for a fragment of English, Syntactic analysis, Augmented grammars , Semantic interpretation, Ambiguity and disambiguation, Discourse understanding, Grammar induction, Probabilistic language processing, Probabilistic language models

Text Books


References

AUTOMOTIVE ELECTRONIC SYSTEMS (404210)

Teaching Scheme

Lectures/Week: 4Hrs

Examination Scheme

Paper: 100 Marks

Unit 1: Power Train Engineering and fundamentals of Automotive.


Unit 2: Sensor technologies in Automotive

In-vehicle sensors: Working principles, Characteristics, limitations and use within the automotive context of the following: Temperature sensing e.g. coolant, air intake, Position sensing e.g. crankshaft, throttle plate. Pressure sensing e.g. manifold, exhaust differential, tyre. Distance sensing e.g. anti-collision, Velocity sensing e.g. speedometer, anti-skid, Torque sensing e.g. automatic transmission, Vibration sensing e.g. Airbags, Flow sensing and measurement e.g. Fuel injection. Interfacing principles: Operation, topologies and limitations of all sensors covered in the above to in-vehicle processing or communications nodes. Interfacing electronics, Operational amplifier circuits, Instrumentation amplifiers, Comparators. Level shifting, Wave-shaping, Filters. Noise mechanisms and reduction. ADCs and DACs. Use of Actuators: Types, Working principle, Characteristics, limitations and use within the automotive context of each type

Unit 3: Automotive Control Systems.

Unit 4: Electronic Control Unit Design.


Unit 5: Automotive Communication Systems

Communication interface with ECUs: Interfacing techniques and interfacing with infotainment gadgets. Relevance of internet protocols, such as TCP/IP for automotive applications. Wireless LANs standards, such as Bluetooth, IEEE802.11x. Communication protocols for automotive applications. Automotive Buses: Use of various buses such as CAN, LIN, FlexRay, Recent trends in Automotive buses (Such as OBDII, MOST, IE, IELLI, D2B, and DS1). Application of Telematics in Automotive: Global Positioning Systems (GPS) and General Packet Radio Service (GPRS), for use in an automotive environment. Higher End Technology: Comparative study and applications of ARM Cortex- A series/M-series, ARM 9 and ARM11. Current developments and issues.

Unit 6: Diagnostics and Safety in Automotive


Text Books

Reference Books

NANOTECHNOLOGY IN ELECTRONICS (404210)

Teaching Scheme                                  Examination Scheme

Lectures: 4 Hrs/week                              Paper: 100 Marks

Unit 1: Introduction

Introduction to Nanotechnology: Fundamental science behind nanotechnology, tools for measuring nanostructures, tools to make nanostructures and imagine nano-behaviours

Unit 2: Nano-CMOS Devices


Unit 3: Nano particles and Nanotubes

Properties of Nano particles: Metal nanostructures and semiconducting nanoparticles, Carbon nanostructures: carbon molecules, clusters, nanotubes, properties of nanotubes-strength and elasticity, applications of carbon nanotubes.

Unit 4: Nanomachines and Nanodevices

Nanomachines and Nanodevices, NEMS and MEMS and their fabrication, molecular and super molecular switches. Lithography.

Unit 5: Nanoelectronics

Introduction, the tools of manufacturing of micro and nano fabrication optical lithography, electron beam lithography, atomic lithography. Nano-Electronics for advanced computation and communication.

Unit 6: Nanotechnology in Electronics:

Use of Nanotechnology in Electronics: Application of nano structures in electronics, sensors, optics, energy capture, transformation and storage. Application of nanotechnology in biomedical electronics.

Reference Books

3. Gregory Timp, _Nanotechnology_, Springer-Verlag NY.
PROJECT(404211)

Teaching Scheme                                  Examination Scheme

Practical: 2 Hrs/Week (Sem _I)                     Term work: 100 Marks
Practical: 6 Hrs/Week (Sem-II)                     Oral: 50 Marks

1. Group Size

The student will carry the project work individually or by a group of students. Optimum group size is in 3 students. However, if project complexity demands a maximum group size of 4 students, the committee should be convinced about such complexity and scope of the work.

2. Selection and approval of topic

Topic should be related to real life application in the field of Electronics and Telecommunication
OR
Investigation of the latest development in a specific field of Electronics or Communication or Signal Processing
OR
The investigation of practical problem in manufacture and / or testing of electronics or communication equipments
OR
The Microprocessor / Microcontroller based applications project is preferable.
OR
Software development project related to VSDL, Communication, Instrumentation, Signal Processing and Agriculture Engineering with the justification for techniques used / implemented is accepted.
OR
Interdisciplinary projects should be encouraged. The examination will be conducted independently in respective departments.

Note:

The group should maintain a logbook of activities. It should have entries related to the work done, problems faced, solution evolved etc., duly signed by internal and external guides.
Project report must be submitted in the prescribed format only. No variation in the format will be accepted.
One guide will be assigned at the most 3 project groups.